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Docket No.: W&B-INF-1973

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/676,588
Applicant : Peter Beer, et al.
Filed : October 1, 2003
Art Unit : 2818
Title : Test System and Method for Testing Memory Circuits
Docket No. : W&B-INF-1973
Customer No. : 24131

S T A T U S L E T T E R

Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450

S i r :

It is respectfully requested that this Status Letter be returned to:

LERNER AND GREENBERG, P.A.
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with an indication as to when an action may be expected.

Respectfully submitted,

Alexia J. Vrahimis

Date: January 20, 2005

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